



Pb Free

RoHS Compliant

### Features

- "Small size", 0.063cc (5×7×1.8mm)
- Using 1 chip HCMOS IC
- 3-state devices are available
- APR (Absolute Pull Range) up to  $\pm 50 \times 10^{-6}$  or  $\pm 100 \times 10^{-6}$

### Applications

- Digital Switching System
- ATM SDH
- SONET
- xDSL

### How to Order

KV7050L 25.0000 C 3 0 A 00  
① ② ③ ④ ⑤ ⑥ ⑦

- ① Type (7.0×5.0mm SMD)
- ② Output Frequency
- ③ CMOS Output
- ④ Supply Voltage: 3→3.3V, 5→5V
- ⑤ Frequency Tolerance  
F→ $\pm 100 \times 10^{-6}$ , G→ $\pm 50 \times 10^{-6}$
- ⑥ Enable / Symmetry 40 to 60%
- ⑦ Individual Specification

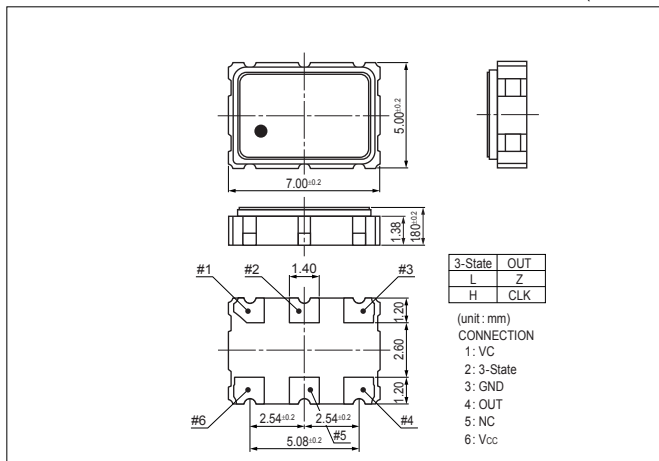
### Specifications

Item	Symbol	Conditions	Specification		Units
			KV7050	KV7050L	
Output Frequency Range	F <sub>o</sub>		7.2 to 50		MHz
Storage Temperature Range	T <sub>stg</sub>		-40 to +90		°C
Operating Temperature Range	T <sub>use</sub>		-40 to +85		°C
Absolute Pull Range (APR)	APR	Included 20years Aging (at +25°C)	$\pm 50/\pm 100 \times 1 (VC=0.5 \text{ to } 4.5V)$	$\pm 50/\pm 100 \times 1 (VC=0.3 \text{ to } 3.0V)$	$\times 10^{-6}$
Max. Supply Voltage	—		-0.5 to 7		V
Supply Voltage	V <sub>CC</sub>		5 $\pm$ 10%	3.3 $\pm$ 10%	V
Current Consumption	I <sub>CC</sub>		15 Max.		mA
Control Voltage	VC		2.5 $\pm$ 2.0	1.65 $\pm$ 1.35	V
Symmetry	SYM	50% V <sub>DD</sub>	40 to 60		%
Rise Time / Fall Time	tr / tf		10 Max.		nS
Output Voltage-"L"	V <sub>OL</sub>		10% V <sub>CC</sub> Max.		V
Output Voltage-"H"	V <sub>OH</sub>		90% V <sub>CC</sub> Min.		V
Output Load	CL		15 $\pm$ 10%		pF

\*1 For  $100 \times 10^{-6}$ , 7.2MHz to 30MHz  
• APR=Absolute Pull Range is the minimum guaranteed (voltage controlled) frequency shift ( $\Delta f/f N_{ow}$ )

### Dimensions

(Unit : mm)



### Recommended Land Pattern

(Unit : mm)

